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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/670,346	09/26/2003	Takashi Miyazawa	117333	5256
25944 7	590 09/30/2004		EXAMINER	
OLIFF & BERRIDGE, PLC			LAM, TUAN THIEU	
P.O. BOX 19928 ALEXANDRIA, VA 22320			ART UNIT	PAPER NUMBER
			2816	2816
			DATE MAILED: 00/20/200	4

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)			
		10/670,346	MIYAZAWA, TAKASHI			
	Office Action Summary	Examiner	Art Unit			
		Tuan T. Lam	2816			
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
THE I - Exter after - If the - If NO - Failur Any r	ORTENED STATUTORY PERIOD FOR REPL MAILING DATE OF THIS COMMUNICATION. nsions of time may be available under the provisions of 37 CFR 1.1 SIX (6) MONTHS from the mailing date of this communication. period for reply specified above is less than thirty (30) days, a reply period for reply is specified above, the maximum statutory period re to reply within the set or extended period for reply will, by statute eply received by the Office later than three months after the mailing ad patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a reply be timely within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE	rely filed s will be considered timely. the mailing date of this communication. O (35 U.S.C. 8 133)			
Status						
1)⊠	1) Responsive to communication(s) filed on <u>30 December 2003</u> .					
2a) <u></u> □	This action is <b>FINAL</b> . 2b)⊠ This	s action is non-final.				
	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Dispositi	on of Claims					
4) ☐ Claim(s) 1-20 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration.  5) ☐ Claim(s) is/are allowed.  6) ☐ Claim(s) 1-20 is/are rejected.  7) ☐ Claim(s) is/are objected to.  8) ☐ Claim(s) are subject to restriction and/or election requirement.						
Applicati	on Papers					
10)⊠ `	The specification is objected to by the Examine The drawing(s) filed on <u>26 September 2003</u> is/s Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct The oath or declaration is objected to by the Ex	are: a) $\boxtimes$ accepted or b) $\square$ object drawing(s) be held in abeyance. See tion is required if the drawing(s) is obj	37 CFR 1.85(a). ected to. See 37 CFR 1.121(d).			
Priority u	nder 35 U.S.C. § 119					
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  a) All b) Some * c) None of:  1. Certified copies of the priority documents have been received.  2. Certified copies of the priority documents have been received in Application No  3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).  * See the attached detailed Office action for a list of the certified copies not received.						
Attachment	(s)					
1) Notice	e of References Cited (PTO-892)	(PTO-413)				
3) 🛛 Inform	e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) No(s)/Mail Date <u>10/10/2003</u> .	Paper No(s)/Mail Da 5) Notice of Informal Pa 6) Other:	te atent Application (PTO-152)			

Application/Control Number: 10/670,346

Art Unit: 2816

## **DETAILED ACTION**

## Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 2. Claims 1-6 and 11-20 are rejected under 35 U.S.C. 102(e) as being anticipated by Sanford et al. (USP 6,734,636). Figure 2 of Sandford et al. shows a pixel circuit comprising a first transistor (Q203), a second transistor (Q201), an electro-optical element (OLED 220), a third transistor (Q202), an electric potential line (lines connected to Vdd1 and Vdd2) connected to the sixth terminal together with the sixth terminals of the other unit circuits of the plurality of unit circuits (not shown), a control circuit (235) for setting the electric potential control line (wire coupled to either Vdd1 or Vdd2) to a plurality of electric potentials (Vdd1 or Vdd2) or to be electrically connected to a predetermined electric potential and electrically disconnected from the predetermined electric potential as called for in claims 1-3, 6, 11-12 and 14-20.

Regarding claim 4, a capacitive element is seen as the capacitor Cs 210.

Regarding claims 5 and 13, the fourth transistor is seen as the switch 235 (column 4, lines 37-39).

3. Claims 7-10 are rejected under 35 U.S.C. 102(e) as being anticipated by Sanford et al. (USP 6,734,636). Figure 3 of Sandford et al. shows a pixel circuit comprising an

Application/Control Number: 10/670,346

Art Unit: 2816

electronic element (OLED 320), a first transistor (Q303), a second transistor (Q301) connected to the first transistor and electrically connecting one of the plurality of second signal lines (lines connected to source/drain of the transistor Q301) to the first transistor by switching to an on state in accordance with a control signal (gate line) supplied from one of the plurality of signal lines, a capacitive element (Cs310), a control circuit (325) connected to the other end of the electronic element, the control circuit controlling current not to flow through the electronic element in a period where current flows through a first current path including the first transistor and the second transistor and to control current to flow through a second current path including the first transistor and the electronic element in a state where the second transistor is in an off state are seen to be inherently present in figure 3 as called for in claims 7 and 9-10.

Regarding claim 8, the capacitive element is seen as the capacitor Cs 310.

## Conclusion

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. In this regard, applicant's cited prior art has been carefully considered.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan T. Lam whose telephone number is 571-272-1744. The examiner can normally be reached on Monday to Friday (7:30 am to 6:00pm).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, TIMOTHY P CALLAHAN can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Art Unit: 2816

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tuan T. Lam
Primary Examiner

Art Unit 2816

9/21/2004